



Design and Development of a Data Logger Based on IEEE 802.15.4/ZigBee and GSM

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Abstract: The Data Logger (DL) is a special device which is designed to accomplish a common task that is to collect data in a targeted area. Some of the DLs have onboard integrated sensors while some collect data using the wireless sensor nodes WSNs. According to the literature some of the proposed DLs are expensive and slow in operation while some do not fulfill all the application requirements i.e. long and short range data logging. Thus a cost efficient, fast and a wireless DLs are the need of a day. In this paper a customized wireless DL is designed, which is cost efficient, fast and can perform in a wide and short range. The designed data logger is based on IEEE 802.15.4 and GSM Technology and use a 32bit microcontroller as a central processing unit. The designed DL is beneficial for the researchers and application developers in data logging and monitoring of environment, weather and road traffic etc.

Keywords: GSM, data logger, TCP/IP, PIC microcontroller, Zigbee

1. INTRODUCTION

Data recording and data analysis play a vital role in the development of new innovations and the prediction of high probable future events. An application such as recording road traffic data, climate change, pollution monitoring, environmental data collection, energy monitoring are now gaining a worldwide attention. A data logger is a device which is used to record and monitor data of a certain process in a specific location.

Using DLs it is now become possible to record and monitor data such as the movements of individual sharks, whales, sea turtles and jellyfish in the bottom of a sea [1]. The data monitoring and recoding period can be as short as several

milliseconds such as monitoring a signal propagation time from source to destination or measurement of the I-V characterization of an electronic device, hence the DL must be very fast that it can monitor and record the transient dynamics of a process [2]. The data logging periods of some processes are very long like monitoring and recoding the environmental parameters, collecting road traffic data, the availability of wind and solar energy, hence the DL must be energy and data efficient such that it can perform for a long time with a battery backup and can store a large amount of data [3]. It is justified that in designing of an embedded systems minimum cost, high speed and maximum power efficiency in a design are primary concern. The

main contribution of this paper is to showcase a DL which is cost and energy efficient and can also perform as a wide and short range wireless data logger.

The development of the proposed data logger is being done in the following sections: Section I gives an introduction, In section 2 some related literature to the proposed work proposed is presented along with prior work. In section 3, The operation of the DL and wireless sensor network and TCP/IP communication of the DL is explored. In section 4, Circuit implementation of the designed DL is presented. Section 5 includes experimentation and testing of the designed DL. Finally, the conclusions and further work are given in section 6.

2. PRIOR WORK

A low cost tele-health monitoring system which is based on a microcontroller and computer based software for an on line data monitoring and recoding is presented in [4]. While a field programmable gate array based super conductor characteristics monitoring system is designed in [2]. A general purpose low- cost, low power consumption, flexible sensor attachment with an optional WI-FI facility and XML-based spatial-temporal DL is presented in [5] and a microcontroller based low cost DLs integrated with humidity or temperature sensors having extended flash memory or SD card was designed in [6-11].

The DLs presented in [2-5] are few expensive and are application specific, while the DL proposed in [5-10] are based on low speed 8-bit microcontrollers and lacks the IEEE802.15.4/Zigbee interfacing facility, which is very essential for wireless data logging in modern systems [12] [13] while in the proposed DL it is included.

Minimum cost, less energy consumption and high speed have great importance in the embedded market, hence design- in an embedded system like

a DL minimum cost, minimum energy consumption and high processing speed are kept in the first priorities, In the proposed DL maximum speed and minimum cost are achieved by using low cost 32 bit microcontrollers i.e. PIC32MX250F128B and jennic JN5148. The use of portable DLs and remote data logging is gaining importance in many applications as in [14-17], hence the designed DL is enhanced by adding Internet access through TCP/IP using Global System for Mobile (GSM).

3. THE PROPOSED DATA LOGGER

The PIC32, JN5148 and GSM module are the main components in the designed DL, PIC32 microcontroller acting as a control unit of the DL. Data from wireless sensor nodes is received by JN5148, PIC32 takes data from the JN5148 through UART2, after the data is successfully received, if GSM services are not available data is than saved on EEPROM and if GSM services are available the data is transmitted on a TCP/IP.

In order to explore the working of the designed DL its data flow and interfacing with a data base and some wireless sensor nodes is presented in Fig. 1.

The designing of the DL is divided in three sections: in the first section the integration of the main control unit i.e. PIC32MX250F128B within the system and its advantages are described, in the second section interfacing of GSM-TCP/IP and JN5148-IEEE 802.15.4/Zigbee is discussed. In III section the block diagram and schematic of the designed DL are presented.

3.1 The Central Processing Unit PIC32

The PIC32MX250f128b is chosen as a centralized control unit for the DL due to the following reasons:

- To achieve the maximum processing speed the microchip PIC32 is selected which is a 32 bit microcontroller with MIPS4000 core and maximum clock speed of 80Mhz, 512 KB programming memory and 32KB RAM [18]

and is relatively four times faster than the 8-bit microcontrollers which are used in [5-10].

- As the minimum cost of the designed DL is required, PIC32MX250F128B is used in the system which is a cost effective solution and all mostly it costs less than 3\$ per chip.
- PIC32MX250F128B is an energy efficient solution, normally it operates at 2.3 V to 3.6 V DC and consumes less than 0.5 mA/Mhz.
- PIC32MX250F128B is DIP-SOC and can easily be integrated on any circuit board.
- To make the designed DL user friendly PIC32 is selected because it supports on board and Graphical User Interface (GUI) based programming, hence one who has limited knowledge about C language can easily implement the complicated algorithms over PIC32 using MATLAB Simulink blockset GUI [19].

The schematic and basic circuit of PIC32MX250F128B is given in Fig.2, while the technical data about PIC32 microcontroller family, which is more important for designing a data logger which is given below:

- MIPS 4000 core, maximum clock speed 80 MHz
- Maximum of 512 KB program memory, 32 KB RAM
- Up to 85 I/O pins and two I2C buses.

3.2 IEEE 802.15.4/ZigBee Implementations

To communicate with wireless sensor nodes in the designed DL the IEEE802.15.4 based Zigbee JN5148 M003 wireless microcontroller is used. The ZigBee is a wireless data communication and networking protocol which is based on IEEE 802.15.4 standard and is primarily designed because of the following reasons as given in [20-21].

- Zigbee is a Low power wireless short range data communication and networking solution.
- Zigbee is a self-organizing wireless data communication and networking option.
- Zigbee based devices are cost effective, highly reliable, short latency, has low rate and has

high capacity as compared with Bluetooth, IrDA, Wi-Fi and GPRS

ZigBee networks can be divided into four layers, in bottom of ZigBee protocol stack we have physical layer (PHL), after PHL media access control layer (MAC) and then network layer (NWK) and application layer (APL), each layer provides data to its upper and management services. More over ZigBee application layer consists of sub-layer (APS), ZigBee device object (ZDO) and manufacturer-defined application objects.

The network based on ZigBee protocol is composed of three kinds of i.e. sensors nodes (End-Device), routing node (Router) and a pool node (Coordinator). The coordinators act as a parent while router and end devices act as a child. To organize ZigBee network there must be at least one coordinator it is the responsible of a coordinator in a network to initialization and maintain a network. End devices acts as a sensor nodes usually they collect data from sensors. The function of Router is to route information in between coordinator and end devices. ZigBee supports Star network (star), tree network (tree) and the mesh network (mesh) topology, the topology is implemented on the bases of application [21].

In the designed DL the IEEE802.15.4 based Zigbee JN5148M003 wireless microcontroller is used, which only support all network topologies. In the proposed DL before doing any wireless sensors data acquisition operation the JN5148 M003 must be programmed as a coordinator.

4. TCP/IP IMPLEMENTATION

The SIM900 GSM module is used to connect the data logger with remote data base through TCP/IP. There are two modes of connection for SIM900 TCP/IP application i.e. single connection and Multi connection. In single connection mode, SIM900 can work at both transparent mode and non-transparent mode and under these two

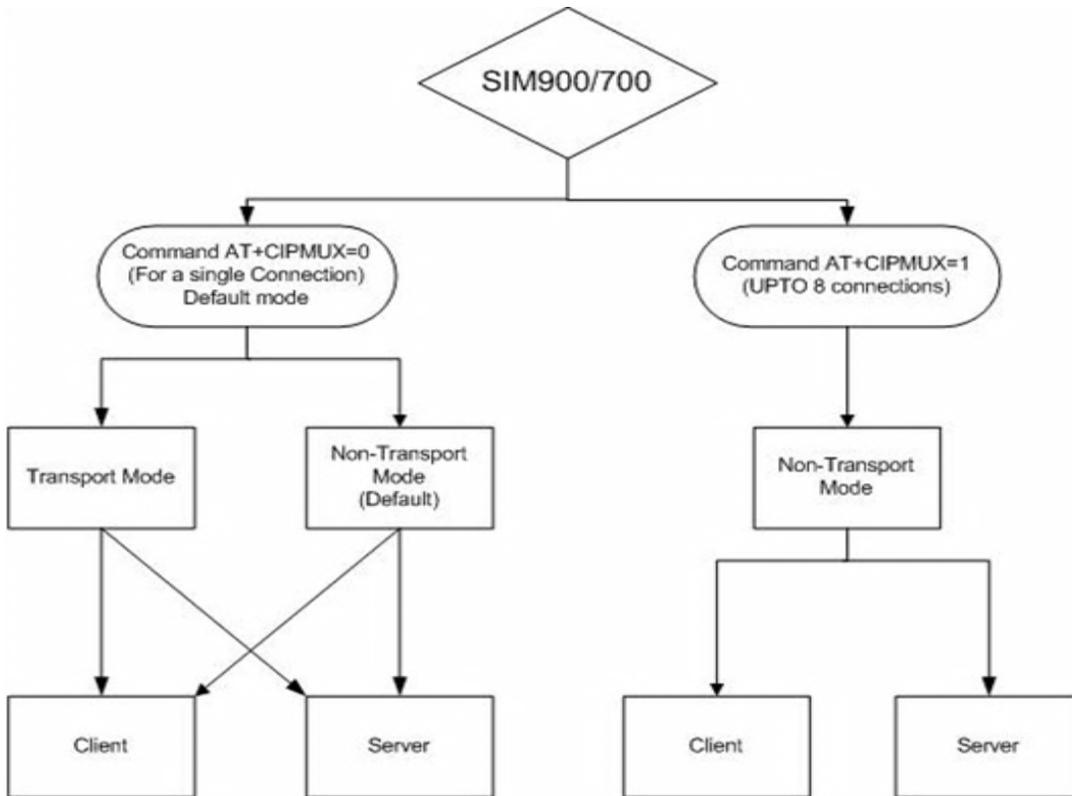


Fig. 3. GSM Modem-SIM900-TCP/IP configuration.

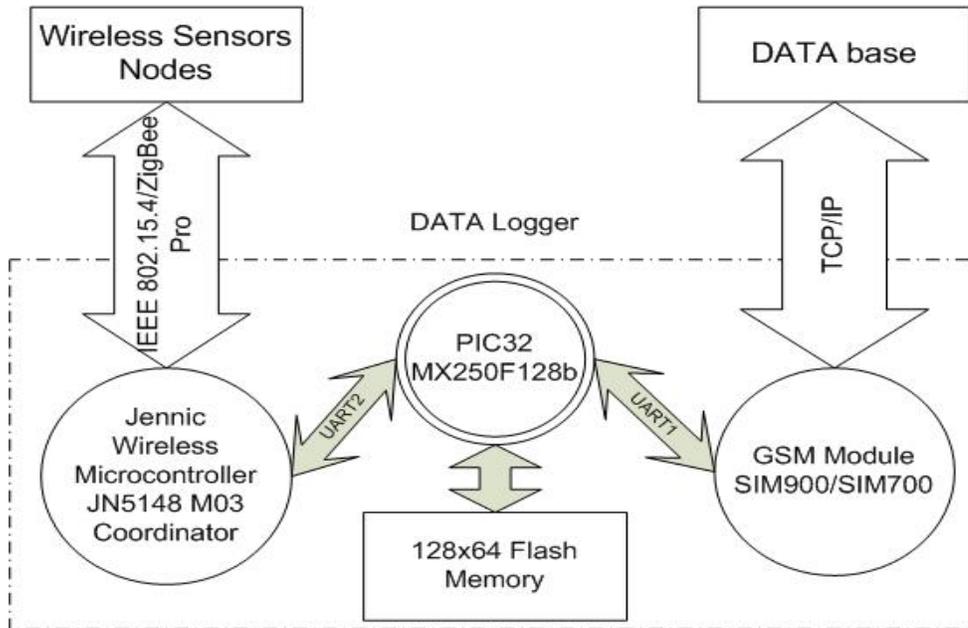


Fig. 4. Working flow diagram of the proposed data logger.

transmission modes, SIM900 can be configured as either TCP/UDP client or TCP server. When in multi connection mode, SIM900 only can work at non-transparent mode. In this mode, SIM900 can work as an absolute TCP/UDP client, which can establish 8 connections in total. In this mode, it also can be configured as one TCP server, which allows 7 TCP/UDP clients to connect in and the TCP server also can act as a client, establishing seven connections to one remote server. The structure of the TCP/IP application is given in Fig. 3.

5. THE SYSTEM DESCRIPTION AND WORKING

The overall working of the data logger is presented in a form of a block diagram in Fig. 4. A wireless sensor network is composed of a coordinator, end device and router. Coordinators act like parents and end node and routing devices acts like childes during a network formation. In any wireless sensor network at least one coordinator is required, in the designed data logger the (Jennic wireless microcontroller JN5148 M003) which is acting as a coordinator and is responsible to collect data from the deployed wireless sensor nodes. The main control unit that is PIC32 has two UARTs one is used to communicate with Jennic microcontroller while other is used to communicate with the GSM module. The coordinator transfer received data serially to the main control unit that is PIC32. As the data is received on PIC32, the control unit checks the availability of GSM services and if GSM services are available the data is transmitted to the data base and online monitoring software, if GSM services are not available the collected sensor data is than stored on the flash and at the same time at regular intervals the control is also checking the GSM services, as soon the GSM services appeared the data stored on the flash is transferred to the data base via TCP/IP. This whole operation of data exchange and storage is commanded by the control unit, the explanation of the operations performed by the control unit is given Fig. 5 in form of a flow chart.

6. HARDWARE FABRICATION

A prototype of the proposed DL is fabricated initially to test the device. A PCB of the proposed DL is designed is a ALTIUM designer software, however no any electronic simulator is available in the market which can simulate the PIC32, Jennic MCU and GSM-SIM900 device there for each and every device is tested and evaluated in real time. To test the data logger each device is programed and tested separately on a computer UART and finally the end product is developed. A photograph of the prototype of the designed data logger is given in Fig. 6. The DL can communicate with a remote data base through a TCP/IP in a case when the hardware is configured as an "A" of Fig. 6, while using the DL as in "B" of Fig. 6 the device only stores data on the flash.

7. SOFTWARE DESIGNING

Monitoring and recoding of meteorological, geographical and transportation information etc. have a great importance in research and development, usually monitoring and recoding of meteorological, geographical and transportation information is accomplished through the data loggers. In literature review it is analyzed that minimum cost, minimum energy consumption, wireless facility, portability and maximum processing speed of a data loggers is the need of a day. The work in this paper was carried out to design a data logger which is a cost efficient, energy efficient, wireless, portable and high processing speed than the data loggers designed in the literatures. Achieving minimum cost, maximum speed and low energy consumption of the proposed design was become possible by introducing the PIC32 and Jennic JN5148 microcontrollers.

The designed DL is wireless and is compatible with IEEE 802.15.4, GSM and TCP/IP and it is also able to perform in both off line and on line modes in off line mode the designed data logger stores data on its flash while in on line mode the data logger send data to the data base.

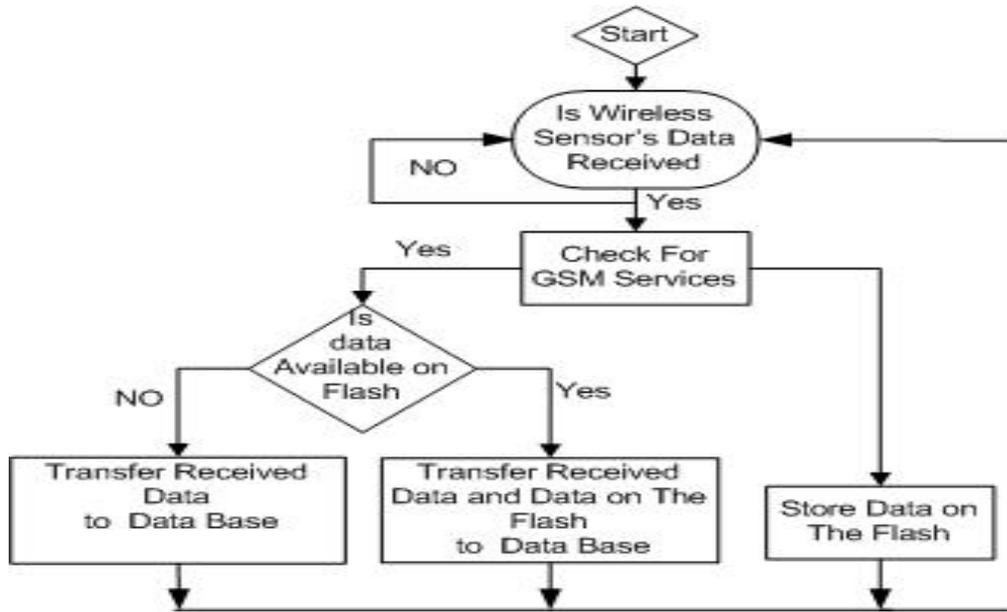


Fig. 5. Operations and functions performed by the control unit.

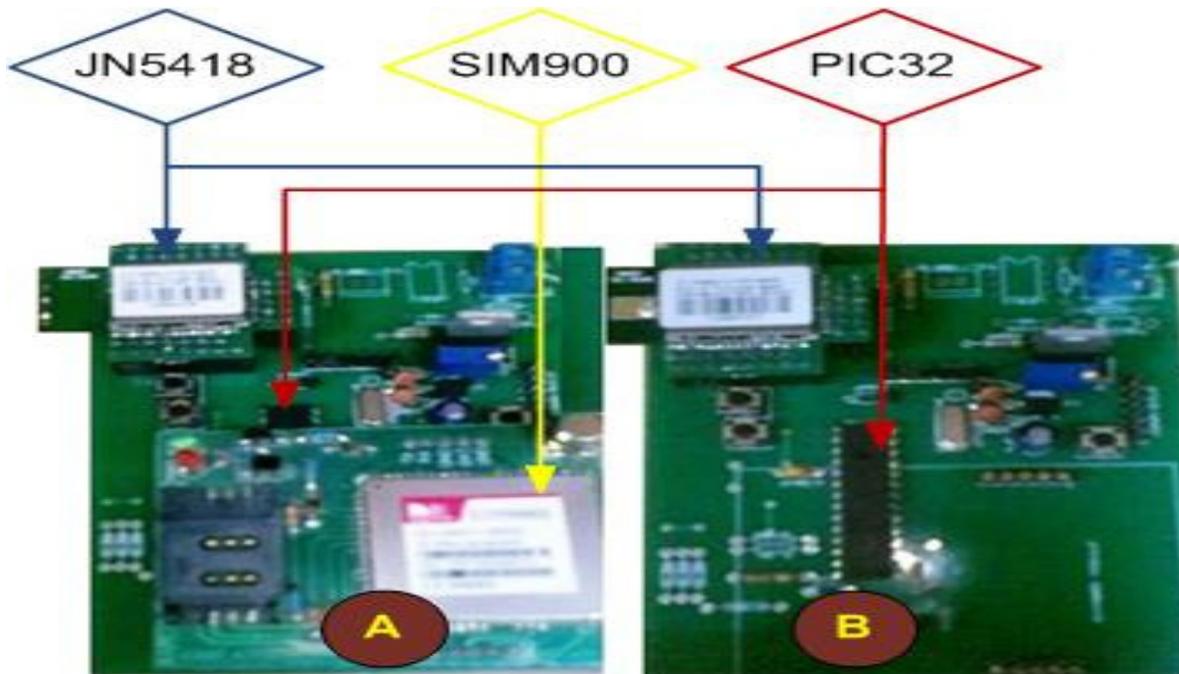


Fig. 6. In Figure “B” the designed DL board is shown without a GSM module while in Figure “A” the GSM module is mounted over the DL board.

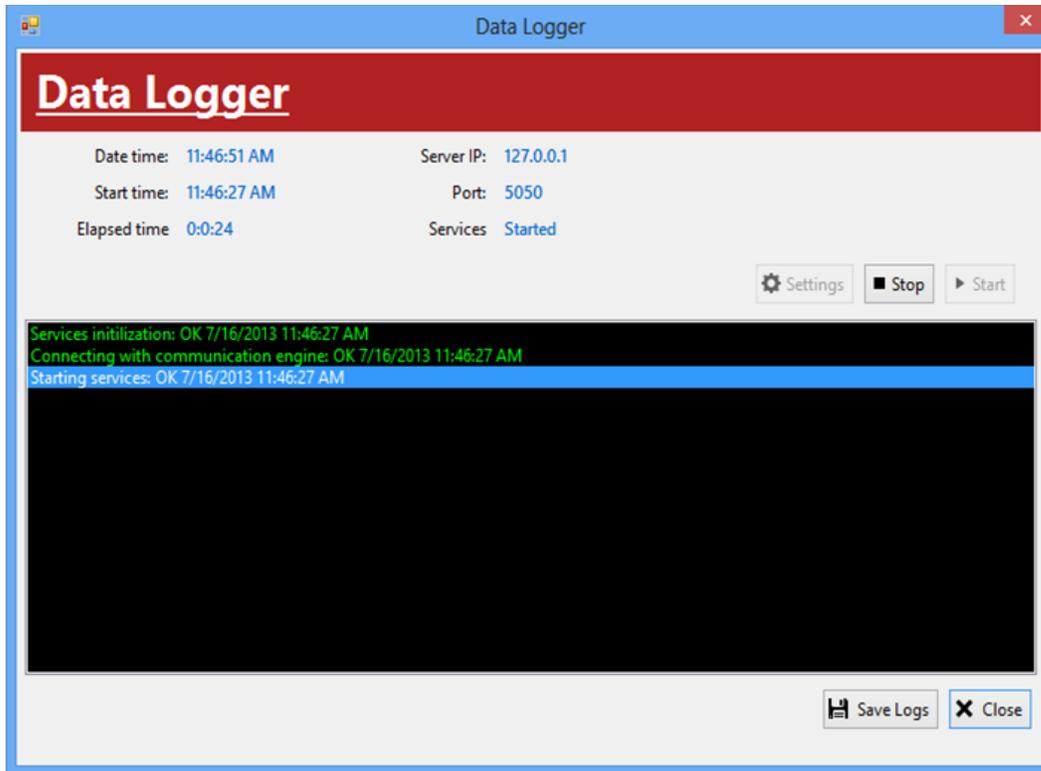


Fig. 7. A screenshot of the data logger software for TCP/IP onfiguration and communication.

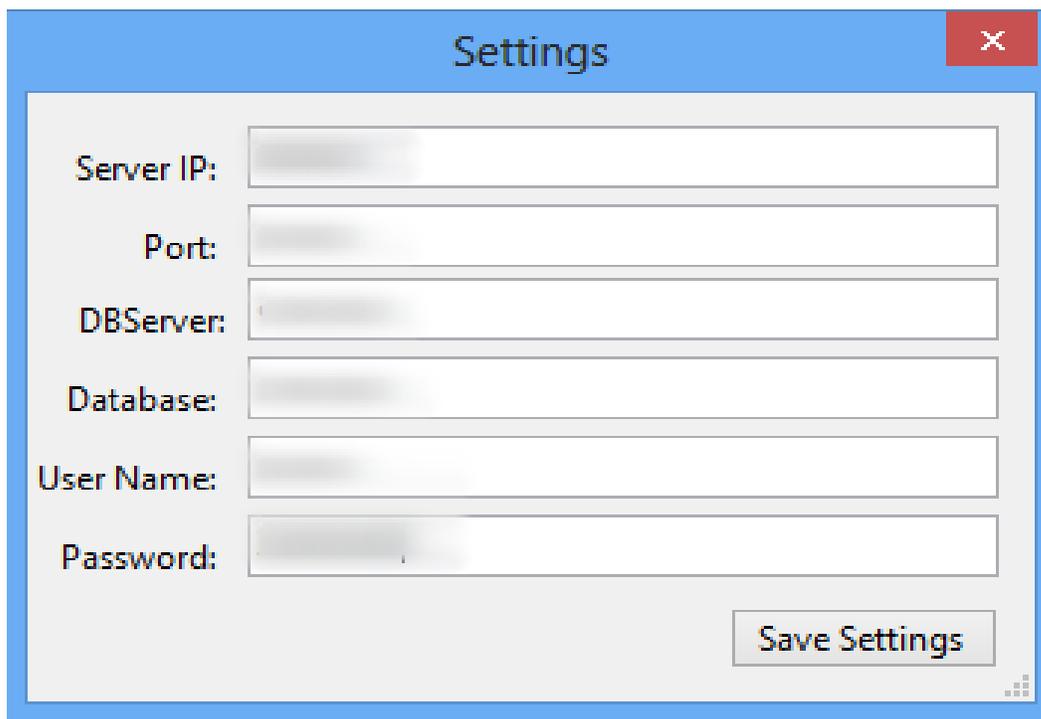


Fig. 8. A screenshot of setting window of the designed software.

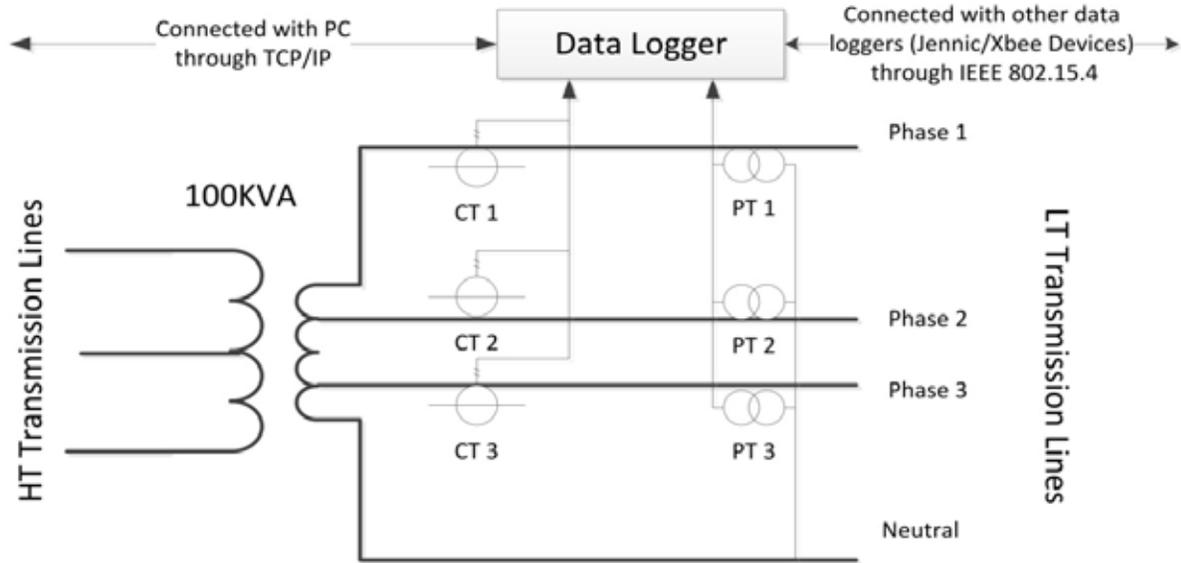


Fig. 9. Schematic diagram of DL operation.

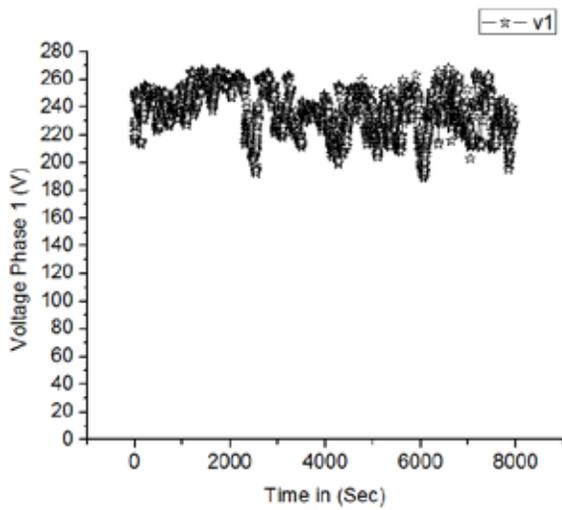


Fig. 10. Voltage of Phase 1 of the transformer.

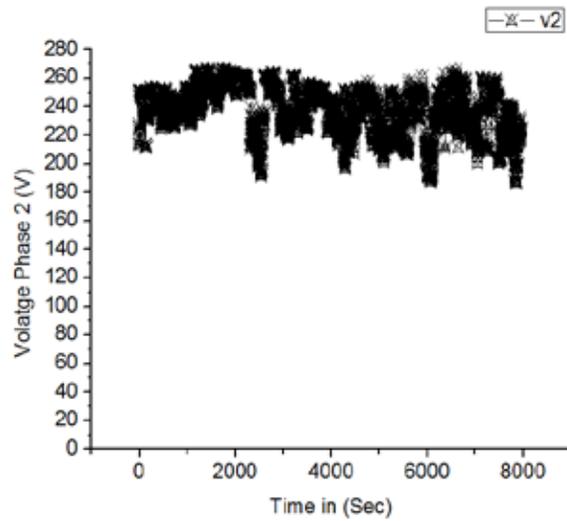


Fig. 11. Voltage of Phase 1 of the transformer.

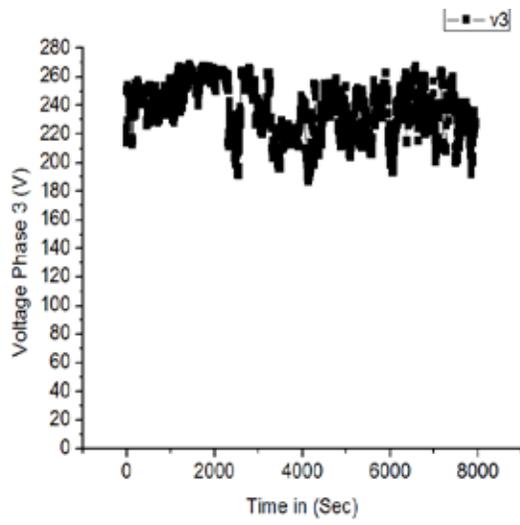


Fig. 12. Voltage of Phase 1 of the transformer.

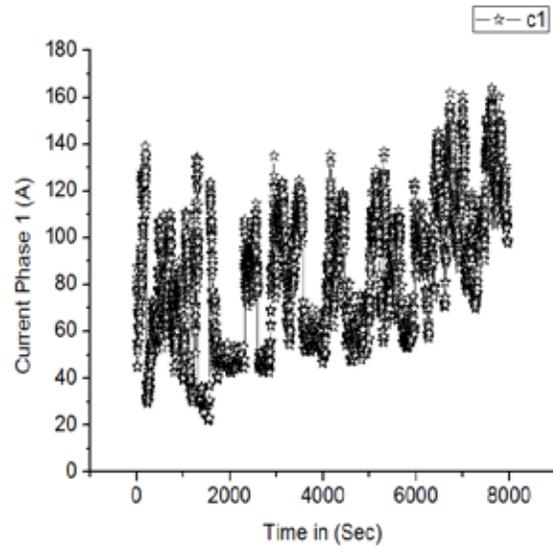


Fig. 13. Voltage of Phase 1 of the transformer.

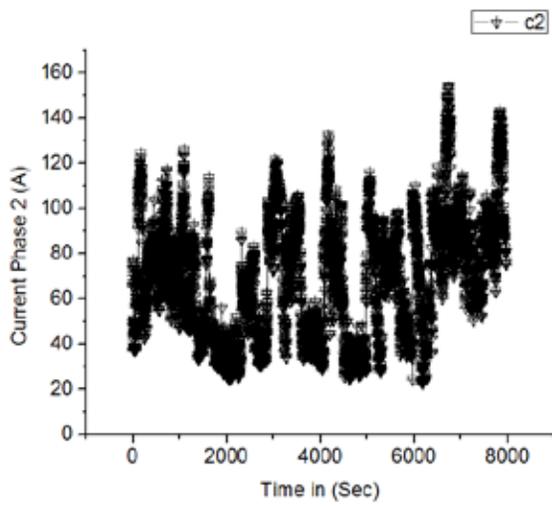


Fig. 14. Current of Phase 1 of the transformer.

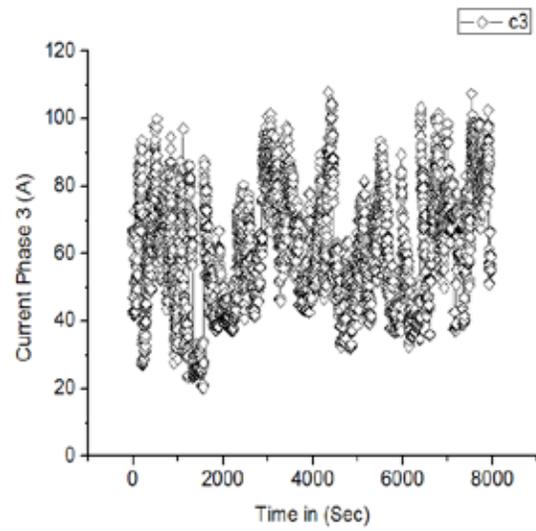


Fig. 15. Current of Phase 1 of the transformer.

Some of the screen shorts of the designed data logger are given in Fig.7&8, which proves its validity. The current printed circuit board PCB of the designed data logger is a single layer in future one can more miniaturize the size of the designed data logger by implementing it on a two or more layer PCB. The power supply of the designed data logger is not rechargeable hence in future it is needed to design a power supply for the designed data logger which is rechargeable from some renewable resource such that one can easily use it in remote applications.

8. DATA LOGGER IMPLEMENTATION AND RESULTS

The designed data logger is connected with a 100KVA transformer in the way as shown in the Fig. 9. The current and voltage of each phase of the transformer was monitored for 8000 Sec (Iteration time 1 Sec). The collected data of the voltage and current is transferred to Computer using TCP/IP module from DL. The voltage data of each phase of transformer, i.e., V1, V2 and V3 are shown in Fig. 10, Fig. 11 and Fig. 12, respectively. Also the current data of each phase of transformer, i.e., I1, I2 and I3 are presented in Fig. 13, Fig.14 and Fig. 15, respectively.

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