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An Emerging White LED Technology and associated Thermal Issues – A Review

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Abstract-- White light emitting diodes (white-LEDs) are the promising light sources of new lighting technology due to their high efficiency, energy saving, long life and environmentally friendly. In this review, different techniques proposed for the development of white-LEDs have been discussed in terms of their performance efficiency and potential usage. The Phosphor based white-LEDs are widely used LEDs, as compared to RGB based white-LEDs, due to their luminous efficacy, service life, display color gamut and the color rendering. Furthermore, thermal issues related to high power white-LEDs have been discussed as it greatly impacts the performance efficiency. To resolve thermal issues, heat sinks having high thermal conductivity are most widely used among others.

Keywords- Light-emitting diode (LED); RGB-LEDs; Phosphor; High power white LEDs; Thermal issues.

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I. INTRODUCTION

• etting light by rubbing sticks/stones was common practice Gin early ages. The initial techniques adopted by humans for indoor lighting were candles and oil lamps [1]. Humphry Davy invented the first arc lamp which was later modified by Foucault. The invention of incandescent lamp and fluorescent lamp has completely changed the trend of lighting [2]. The invention of fluorescent lamp was a big milestone especially for indoor lighting. The development of high-intensity discharge lamps replaced these old technologies; however, some lamp types could not enhance the efficiency more than 70% [3,4]. The struggle for the efficiency enhancement leads to the invention of LEDs. It has been considered as an encouraging fourth generation light source due to its long life, high luminance, less weight, smaller size, optical efficiency, less power consumption and environmentally friendly [5]. Lifetime of LEDs is 5 times more than fluorescents lamps and 25 times more than incandescent bulbs [6]. LEDs are replacing incandescent and fluorescent lamps due to major factors like absence of mercury and higher efficiency for indoor lighting. About 10.3% electricity of commercial sector in USA is dedicated to lighting and 6.2% of residential sector, as estimated in 2018 [7]. If all the traditional white light sources are changed to white-LED, power utilization can be minimized by 1,000 TWh/year [8].

LEDs are not inherently white light source and are developed by different techniques due to their wide applications [9]. White LEDs can be developed using Red-green-blue (RGB) and phosphor coating. Nichia Chemical Industries has developed blue LED using Gallium Nitride (GaN) which has been considered a milestone in the development of white-LED [10]. In this study, different configurations to develop white-LEDs by using blue LED chips are briefly discussed. Thermal stability is a major issue especially in high power LEDs. Furthermore, different thermal models are comprehended along with their drawbacks and solutions in high power LEDs. Heat sinks play a vital role in high power LEDs to dissipate heat to the surroundings.

II. WHITE-LED TECHNOLOGY

LED is a semiconductor diode that radiates energy (light) under forward biased condition. The core of LED is a chip having p-side and n-side. The electrical properties of a semiconductor can be changed by introducing impurities by a process called doping. When a semiconductor is doped at different locations with different materials, a junction is formed between two areas of differently doped materials. When the diode is forward biased, electrons from n-type region moves towards p-type region. The emission of photons (light) occurs due to the recombination of holes and electrons [11]. The electrons from the conduction band (E_c) jumps to valence band (E_v) and emit light, as shown in Fig. 1. In figure E_f is the fermi level.

The wavelength of radiation depends on energy band gap (E_g) . Planck-Einstein's equation describes this phenomenon by relation 1 [11].

$$E = hv = hc / \lambda = E_g \tag{1}$$

where *E* is energy of photon, *h* indicates the Planck's constant, v is photon frequency, λ is wavelength of emitted light and *c* specify the speed of light.



Fig. 1. Light generation principle of LED.

The color of light depends on the energy gap and energy gap governed by semiconductor material. If energy gap increases, the frequency of emitted light increases and, thus, wavelength decreases. LED can emit the electromagnetic radiations in invisible spectrum. [12]. Table I summarizes the different wavelengths and their emitting materials [12,13].

TABLE I : EMITTED COLOR OF LED WITH WAVELENGTH AND MATERIALS

| LED light | Wavelength (nm) | Material | |
|-------------|--------------------|---|--|
| Infra-Red | >720 | Gallium arsenide (GaAs), Aluminum gallium arsenide (AlGaAs). | |
| Red | 625-720 | Gallium phosphide (GaP), Gallium arsenide phosphide (GaAsP), Indium gallium phosphide (InGaP). | |
| Orange | 600-625 | Gallium phosphide (GaP), Aluminum gallium indium phosphide (AlGaInP), Gallium arsenide phosphide (GaAsP). | |
| Yellow | 570-600 | Gallium phosphide (GaP), Aluminum gallium indium phosphide (AlGaInP), Gallium arsenide phosphide (GaAsP). | |
| Green | 515-570 | Gallium nitride (GaN), Aluminum gallium phosphide (AlGaP). | |
| Blue | 455-490 | Aluminum indium gallium nitride (AlInGaN), Indium gallium nitride (InGaN), Gallium nitride (GaN). | |
| Ultraviolet | <390 | Indium gallium nitride (InGaN), Diamond (C), Aluminum gallium nitride (AlGaN) | |

To operate LED in daily life applications, diversity in LED chips have been proposed, namely conventional, vertical injection and flip chips. In conventional chip, p and n electrodes are positioned on top of the LED chip and sapphire is used as a substrate. The electrodes in vertical injection chip are positioned at top and base of the chip, and silicon is used instead of sapphire substrate to enhance thermal conductivity. On contrast with conventional type, it has reduced series resistance and improved power transformation effectiveness [14-16]. Flip chip is an inverted structure of conventional chip having both electrodes at base of the chip and light is radiated from top of the structure which evades light retention by the metal as in the case of conventional chip. Flip chip structure LEDs own better thermal characteristics. Lu et al. [17] presented a thermal analysis on high power flip chip LED. The different chip structures are shown in Fig. 2 where MQW is multiple quantum well.



Fig. 2. Different types of LED chips [18].

White light is the basic requirement in illumination but there is no such LED that emits white light inherently. White light emitting LED (white-LEDs) can be obtained in two ways; 1) RGB-LEDs system. 2) Phosphor coating method [19]. White-LEDs with phosphor coating method are extensively developed by utilizing blue LED chips and widely used for commercial purposes.

There are few parameters that determine the efficiency of LEDs such as color rendering index (CRI), light extraction efficiency (LEE), color temperature (CT), luminous efficacy [20]. International commission of illumination defines the CRI as the effect of an illuminant on color appearance of the object by conscious or subconscious comparison with its color appearance under a reference illuminant [21]. Color temperature is the temperature of ideal black body that radiates light whose color resembles to given light source [22]. Luminous efficacy is the ratio of luminous flux to input power and measured in lm/W [23]. Light extraction efficiency (LEE) tells us how much photons generated in active region are escaped into free space [24]. To enhance the productivity of LEDs, many techniques have been introduced and immense research further suggesting the new methods have been recorded.

III. RGB BASED WHITE-LEDS

The wavelength of electromagnetic radiations visible to human eye is about 380-770 nanometers and white light is the mixture of different wavelengths of electromagnetic spectrum in the visible portion [25]. It is a well-known verity that white color LED can be produced by blending the three colors; red, green and blue emitting from different LEDs [26,27]. This is the straightforward method. However, this technique requires complex electrical design to control light intensity since blending the correct ratio of these three colors will produce white light. Chromatic diagram can be used to represent it and CIE 1931 coordinate axis is the most commonly use chromatic diagram [28]. The diagram shows all the colors visible to average person. If some two-color points are chosen on chromaticity diagram, then any color that lie in straight line joining these two points can be formed by blending these two colors. Similarly, by combining coordinates of three different colors, it is possible to produce all colors that lie inside triangle formed by source points [29]. The ideal white point (W.P) on CIE chart has coordinates (0.33,0.33) [30]. The CIE 1931 chromaticity diagram with ideal white point is shown in Fig. 3.



Fig. 3. CIE 1931 chromaticity diagram of the visible gamut [29]

The light source must have the required color point. CIE 1960 chromaticity space is mostly utilized to notice minor color differences [29]. If an area is lightened by more than one lamp, the color variation among all lamps should be close to the minimum-perceptible-color-difference (MPCD) [31].

$$\Delta uv = \sqrt{(u - u_o)^2 + (v - v_o)^2}$$
(2)

where (u, v) are coordinates of light source and (u_o, v_o) are required coordinates of light source.

The color flexibility is the advantage of this technique over phosphor coating technique. It means that, by changing the color point (u, v), the shade of white color can be changed and multi colors are achieved. The facility of varying the color point of lamp impart new attributes in illumination that can possibly create new applications and henceforth new market openings. However, the realization of RGB based white-LEDs encounters some exacting problems.

The white light created by mixing lights emitted from different LEDs has major issue that variation in one color will produce significant change in color point (u, v). Multiple LEDs must possess same color point which is a challenging task as with time and temperature variation may occur in color point. As discussed earlier, LED is a semiconductor device and its

energy band gap is affected by temperature which results in change in color output of LED. A proper control system is required to regulate color outputs which add expense. Temperature, spectra responses mismatch and inverted sensor matrix affects the precision of color output. These drawbacks can be avoided by flux feedback (FFB) and temperature feedforward (TFF) methods but these methods are dependent on complex flux measuring techniques [32]. The luminous efficacy of the white light decreases with increasing temperature. The generation of white light from this method has usually low CRI score [33].

IV. PHOSPHOR COATING BASED WHITE-LEDS

The well-known strategy to produce white-LED is the phosphor coating use, when phosphor layer is fabricated onto blue or ultraviolet chip [34]. The cerium doped yttrium aluminum garnet (YAG: Ce) phosphor is being commonly coated on blue chip to develop a stable white light [35]. The light color properties such as angular color uniformity (ACU). CRI, LEE, correlated color temperature (CCT) are dependent on phosphor characteristics [36]. The phosphor coating based white-LEDs use phosphor whose CRI score is relatively high (70-90+) [37]. Generally, the ultraviolet LED chip is less stable and efficient than blue chip. The III-Nitride semiconductors are remarkable for illumination applications and development in energy efficient high-quality white-LEDs [38-42]. The combination of III-Nitride elements and appropriate downconversion phosphor can produce white light. Upon blue light emission, it is partly absorbed by phosphor coating and remaining part is blended with yellow light of phosphor to produce white light [43,44]. This concept is shown in Fig. 4.



Fig. 4. White light generation with blue LED and yellow phosphor [45]

The precise proportion of blue and yellow light relies on the amount and thickness of phosphor coated on blue LED chip [45]. Several researches have optimized phosphor layer thickness, region of deposition, phosphor concentration, particle size, light extraction efficiency which make phosphor coated LEDs more efficient [45-48]. The AlGaN based deep ultraviolet LEDs face major light extraction problems. The light extraction efficiency is associated with light back-reflected to LED chip which additionally influence the thermal properties of phosphor coated LED by increasing the junction temperature [49,50]. Guo et al. [51] demonstrated a sidewall reflection method which increases light output power (LOP) of 278nm

LEDs by 30%. The geometry of phosphor layer greatly affects optical properties of white LEDs [52]. In general, three configurations are renowned for yellow phosphor coating which are: 1) dispersed or conventional 2) conformal 3) remote. These different configurations provide different optical properties [53].

A. Dispersed Configuration

In dispersed phosphor configuration, phosphor powder is mixed with slurry and suspended in silicon solution which than deposited on the surface of LED chip. This method has main drawback of non-uniform distribution of phosphor, as shown in Fig. 5.



Fig. 5. Different configuration for phosphor coating.

a) Dispersed or conventional coating. b) Conformal coating. c) Remote coating [53]

The dispensing method is used due to low cost phosphor layer, but, as phosphor coating geometry is of spherical shape, it forms a yellow ring with discomfort for eyes. The conventional dispensing method also leads to poor angular color uniformity (ACU) [54]. The settling of phosphor requires for flat surface and, without the provision, it causes a disharmony of blue and yellow lights [55]. The total energy trend of a liquid droplet is always towards minimization and, the shape of liquid droplet, at equilibrium, on a substrate varies for different boundaries [56,57]. Meanwhile, the uniform phosphor coating can be reached in dispersed configuration by adjusting slanting angles of reflector cup, as shown in Fig. 6.



Fig. 6. Different phosphor geometries in reflector cups a) Convex phosphor shape. b) Flat phosphor shape. c) Concave phosphor shape [56]

To overcome the drawbacks of dispersed configuration, the conformal and remote configurations are mostly preferred which are briefly discussed as follows.

B. Conformal Configuration

Conformal configuration is a well-known phosphor deposition method to control thickness, shape and particle size of phosphor [58-60]. In this way, phosphor is deposited uniformly on outline of blue LED chip, as appear in Fig. 5(b). The conformal phosphor coating of thin phosphor layer can benefit in phosphor layer temperature reduction that increases color stability [61].

ACU is classified as an important parameter to judge light standards which are generally required high in lighting applications. Inappropriate ACU distresses the eyes, limiting the uses of white LEDs [62]. The phosphor geometry in white LEDs influences the ACU level. The conformal coating provides uniform phosphor layer thickness needed to obtain high ACU [63]. It not only provides better luminous characteristics but also enhances angular correlated color temperature [64]. The conformal coating can achieve good optical performance, but this method creates 50-60% backscattering light from phosphor layer that decreases the luminous efficacy [65]. Different approaches have been made for conformal deposition which include slurry, self-exposure, electrophoretic deposition, pulse spray, spin coating, wafer level coating and ionic wind pattern [66,67]. The self-exposure, electrophoretic and slurry methods may yield an even phosphor layer, however the chemical procedures may produce chromium ion pollution [68]. Li et al. [64] discussed pulse sprayed conformal phosphor coating wherein light quality of LED has been improved both in multilayer and a mixture of nitride and yellow phosphor. The angular CCT distribution is improved with maximum CCT variation of 257-225K for multilayer Y-N LED and mixed N/Y phosphor LED, respectively, which are much lower than that for conventional deposition method. Huang et al. [60] also reduced the color deviation of white LED by pulse spray method. The electrophoretic phosphor deposition (EPD) is another famous method when charged particles in suspension are moved under applied electric field. These charged particles are deposited on oppositely charged electrode. As compared to traditional

coating, EPD provides better phosphor thickness control along with simple way to reach the conformal configuration with improved color uniformity of LED [69].

An enhanced EPD strategy of wafer level white LED packaging has been proposed in [70]. The results show luminous efficiency of 3.1 lm/W and CCT deviation 256 K which is an exceptional uniformity in CCT and luminous efficiency. Choi et al. [71] performed an experiment to develop a simpler method of conformal phosphor deposition named "quasi-conformal phosphor dispensing". This method saves 50% phosphor, as compared to conventional phosphor deposition methods. Wu et al. [67] discussed the conformal phosphor coating on ionic wind principle based on their experiments. There results show large optical performance enhancement of white LED and reduced angular CCT deviation from 1630 K to 366 K.

C. Remote Configuration

The remote phosphor method is characterized by a physical separation between LED chip and phosphor layer, as displayed in Fig. 5(c). In conventional and conformal methods, phosphor has direct contact with LED chip that results in high thermal effect on LED chip. The thermal problems may result in lower output light power under high current operation. The phosphor response profoundly relies upon the temperature of LED chip, and, while temperature increases, its emission efficiency decreases. This issue can be solved by remote configuration where a separation exists between the chip and phosphor layer [72,73]. But, despite of the distance, an increase in temperature in phosphor layer affects the life of remote phosphor LED [74]. The thermal isolated encapsulant layer can successfully diminish heat transfer from LED chip to phosphor layer, thus resulting improved luminous efficiency and reduced color variation [75]. Conformal coating has major issue of backscattering light from phosphor layer, which is solved in remote configuration. The remote phosphor configuration reduces the phosphor emission over the LED chip to enhance the luminous efficiency. For maximum light output, the phosphor layer must have appropriate distance from LED chip and backscattered light should be extracted before it is absorbed. The separation between the LED chip and phosphor layer should be long enough so that only small amount of reflected light returns to the chip [76]. A scattered photon extraction (SPE) method has been proposed for remote configuration which shows 60% improvement in light output by efficient extraction of backscattered light, as compared to white LEDs having phosphor positioned closely to chip [77]. In 2005, another scattered photon extraction (SPE) configuration was introduced where optics were used between remote phosphor layer and LED chip and this shows 60% improvement in light output [78]. Despite the separation between phosphor layer and LED chip, there are chances that light rays emitted from phosphor layer after being reflected from reflector cup are assimilated by LED chip, as shown in Fig. 7 [79].

To solve this issue, Kim et al. [80] designed a method with diffused reflector cup and remote phosphor configuration. The

results show that, as compared to conventional method, their remote method with diffused reflector cup can increase light efficiency by 75%. Lin et al. [81] presented another method by use of inverted cone lens encapsulant and nearby ring remote phosphor layer. The proposed geometry reduced the chances of backward light to be absorbed by LED chip. The results show that light extraction efficiency increased by 93%.



Fig. 7. Light rays reflected from reflector cup and absorbed by LED chip [79].

In remote phosphor configuration, the phosphor nonuniformity may happen because of concave surface encapsulant. The angular dependent optical path causes extra yellow rays which creates a yellow ring outside the LED boundary [82]. A method to achieve high uniformity of angular CCT in white LEDs using ZrO₂ nanoparticles is reported on and this improve deviation in CCT by 58% in comparison to traditional remote phosphor method. The ZrO₂ nanoparticles can also solve yellow ring issue in white LEDs and could develop high quality lighting source [83].

V. THREE BAND WHITE LED TECHNOLOGY

The traditional strategy to deliver white light with blue LED and yellow phosphor (YAG: Ce) does not produce good CRI [84]. Three band white LED is an interesting technique to produce white color LED with high CRI [85,86]. This technique incorporates blue LED coated with red and green phosphor. By the emission of blue light from blue LED, green and red phosphors emit green and red light, respectively. The blue light will only partly be absorbed by phosphors and remaining unabsorbed light combines with red and green light components that results in white light [87]. This technique has an apparent advantage of high CRI and color temperature, as compared to blue LED with yellow phosphor method. The recent experimental results show that three band white LED is extra optically better than YAG: Ce coated white LED [85]. The wide color gamut backlight unit (BLU) using three band white LED has been proposed in [88]. The results show that three-band white LED backlight unit (BLU) color gamut increased by 16%, as compared to conventional white LED BLU. Thus, by three band white LED high color gamut BLU with low cost can be achieved. To obtain more efficient LED, designed methods proposed by different researchers are summarized in Table II.

| | TABLE II: METHODS TO IMPROVE EFFICIENCY OF WHITE LED | | | | |
|--------------|--|---|--|--|--|
| Refs. | Proposed Methodology | Consolidated results | | | |
| Joshi et al. | CdAG: Ce prepared by combustion synthesis coated on | Improved CRI up to 87. | | | |
| [89] | blue LED of 470nm chip. | | | | |
| K. Lee, S. | Screen-printing approach based, new uniform yellow | Regulate chromaticity of blue LED. | | | |
| Lee [90] | phosphor coating method is developed in which silicon | | | | |
| | mold plate is fabricated which serves as printing mask. | | | | |
| Chung et | CdSe/ZnSe nanoparticles phosphor is coated on 460nm. | The luminous efficiency of shorter wavelength LED is | | | |
| al. [91] | 430 nm and 400 nm InGaN chips to fabricate white | enhanced i.e. 400nm .430nm and 460nm LED chip at 20 | | | |
| | LED | mA is 6.57 3.96 2.15 Im/W respectively | | | |
| Vu et al | White LEDs with microstructure array on top surface of | The light extraction efficiency (LEE) is enhanced as | | | |
| 10 Ct al. | phosphor silicon lavor | compared to flat phosphor silicone layer i.e. LEE | | | |
| [92] | phosphor sincon layer. | enhanced by 12% (simulation) and 10 1% (experimental) | | | |
| Alving of | ZnSa: Mn/ZnS dened acre nencervetal (NCs) and with | Very high quantum afficiency (OE) 01.0% is achieved in | | | |
| AKIIIS EL | Zilse. Wil/Zils doped core hanocrystal (NCS) and with | both asses | | | |
| al. [95, | quantum dot (QDs) are synthesized separately. | both cases. | | | |
| 94] | | | | | |
| Hu et al. | Bi-layer phosphor model is developed with upper | Lager concentration of upper phosphor layer than lower | | | |
| [95, 96] | phosphor layer having concentration of 0.4 g/cm ³ and | phosphor layer, increases light extraction efficiency | | | |
| | lower phosphor layer with 0.05 g/cm ³ concentration. | (LEE). | | | |
| Zhou et | A coating method having two phosphor layers is put | Superior stability of blue to yellow proportion at different | | | |
| al. [97] | forward with conventional convex coating. | angles. Reduces the CCT deviation by 44%. | | | |
| Lan et al. | ZrO ₂ Nano-particle and red phosphor are added in | ZrO ₂ Nano-particles increase light scattering ability. ZrO ₂ | | | |
| [98] | encapsulant. Different packaging structures of LEDs are | Nano-particles doped with YAG phosphor enhance | | | |
| | formed and investigated. | luminous flux by 7.7% compared to conventional YAG | | | |
| | | doping. ZrO ₂ Nano-particle doped in encapsulant with | | | |
| | | conformal YAG phosphor LED chip enhance luminous | | | |
| | | flux by 19.8% compared to above packaging. Doping | | | |
| | | ZrO ₂ particles with YAG and red phosphor improve CRI | | | |
| | | up to 90. | | | |
| Liu et al. | A silicon-based substrate possess low thermal resistance | Thermal resistance reduced as compared to ceramic and | | | |
| [99] | has been proposed and tested for LED packaging. The | lead frame substrate by 1.965 and 0.589 K/W respectively | | | |
| | substrate is composed of light-reflection cup with silver | and light output efficiency also increase for silicone-based | | | |
| | laver coating. | package. Less costly. | | | |
| Zhang et | Three multicolor phosphor films having CCT 3000k. | High CRI and high luminous efficiency are achieved. The | | | |
| al. [100] | 4000k. 5000k are prepared by mixing monochromatic | experimental results show the decrease in luminous | | | |
| un [100] | phosphor powders in silicone | intensity by heat treatment from 30°C to 150°C | | | |
| Sun et al | A new structure of spatial-coded coating phosphor | Enhances the efficiency of white LED For CCT around | | | |
| [101] | having plates of two sub regions (vellow phosphor and | 6500K efficiency is 68% (experimental) and | | | |
| [101] | open windows) is proposed | 71.8%(simulation) | | | |
| Chan at al | White LED packaging comprise of long anconculation | I ED packaging is accompanied by high reflectivity of | | | |
| [102] | while LED packaging complise of lens encapsulation, | LED packaging is accompanied by high reflectivity of | | | |
| [102] | reflectivity of cavity reflectors and phosphor | cavity reflectors to enhance efficiency. | | | |
| | concentration are investigated with simulation and | | | | |
| IZ CI | corresponding experiments. | | | | |
| K. Chen | Hybrid warm white high voltage light emitting diodes | Luminous efficiency of warm hybrid HV-LEDs increases | | | |
| et al. [103] | (HV-LEDs) are examine by utilizing YAG phosphor, | as compared to conventional cool and warm white LEDs. | | | |
| | INGAN and AlGaINP HV-LEDs. | Significant improvement of CRI, uniform angular CCT is | | | |
| | | observed. | | | |
| Lo et al. | Innovative remote phosphor deposition method is | Uniform white light distribution is achieved with low | | | |
| [104] | proposed including pre-encapsulated silicon lens. | manufacturing cost. | | | |
| Yu et al. | A double remote micro-patterned phosphor film (double | Double RMPP film, enhance the angular color uniformity | | | |
| [105] | RMPP film) is presented. Micro-patterned (MP) is | (ACU). Results show that this configuration exhibit better | | | |
| | manufactured by spin coating and blend with remote | CCT stability than conventional remote phosphor and | | | |
| | phosphor film on both sides. | single RMPP film. | | | |
| 1 | | | | | |

VI. HIGH POWER WHITE LEDS AND THERMAL ISSUES

high power white-LEDs [106]. The applications of high-power white-LEDs increase day by day and it is expected that the highpower LEDs will become leading illumination technology before 2025 [107]. More heat dissipation takes place by

The white-LEDs which operates over 1 watt are known as

increasing the power in LED chip. Thus, thermal management in high power white LEDs is the key issue. High temperature may cause the irreversible damage to phosphor layer, lifetime reduction, wavelength shift and reduce efficiency of phosphor emission [108,109]. The amount of heat produced by phosphor layer should be considered while designing the high-power white LEDs. In phosphor, about 8% of electrical power input is transformed into heat, and, thus, thermal execution of highpower white-LED is affected due to phosphor temperature [110]. Chen et al. [111] prepared LSN Ce and MLSN Ce phosphors and showed that they have better thermal stability than YAG: Ce phosphor for high power warm-light white LEDs. A new ceramic phosphor for high power white LED which ensure better thermal stability and high quantum efficiency has been proposed in [112]. LEDs operating at high power with high junction temperature have great issues on reliability and their performance is associated with thermal management ability [113]. The reliability of LED has direct relation with junction temperature. The greater the temperature of junction, the shorter will be the lifespan of LED. During prolonged usage at elevated operating current (>350mA), a high temperature (150-200°C) originates at junction in high power white LEDs [114,115]. The high temperature of junction led to yellowing of organic encapsulants which results in reduction of luminous efficacy and disgrace long-term reliability [116]. To overcome these issues, inorganic encapsulants have been used as substitute [117]. The LED failure cases are mostly temperature related. The junction temperature of LEDs is primarily influenced by following three parameters; LEDs ambient temperature, thermal path between the ambient environment and LED junction, and power dissipated by LED [118].

VII. THERMAL MANAGEMENT IN LEDS

A typical LED lighting system is facing the issues like decrease in thermal resistance from junction to the substrate, and the accessibility of the orientation free, inexpensive thermal solutions. The efficacy of solid-state lighting devices relying on the temperature of the junction as given in Fig. 8 [119].



Fig. 8. Light output versus junction temperature [119]

The major heat cause in LED is its generation in active region due to non-radiative recombination process and joule heating at interconnects and series resistance of diode [120]. A typical LED illumination system consist of high brightness packages joined with substrate situated on a heat sink. Heat transfer to the ambient environment takes place through different metallic/non-metallic parts of the package, interface material and heat sinks.

Thermal dissipation path of LED is characterized by thermal resistance which basically shows the ability of system to dissipate heat and is defined between two points as the ratio of temperature difference to the power dissipated along the device. Thermal resistance is usually denoted by R_{th} with units of K/W or °C/W and can be obtained by equation 3 [121,122].

$$R_{th} = \frac{\Delta T}{P_d} \tag{3}$$

where ΔT represent temperature difference between the two ends while P_d shows total power dissipated along device. High power LED is commonly placed on metal-core printed circuit board coupled with heat sinks as shown in Fig 9.



Fig. 9. Heat sink

LED thermal resistance model including heat sink as shown in Fig. 10. Heat from LED junction to heat sink is transferred by conduction while heat from the heat sink spread to ambient surrounding by convection.



Fig. 10. Thermal Model

The junction temperature T_j is defined by equation 4 [123,124].

$$T_j = T_a + (P_d \cdot R_{j-a}) \tag{4}$$

 T_a is the ambient temperature, R_{j-a} is the thermal resistance from junction to ambient which can be found by following equations.

$$R_{j-a} = R_{j-h} + R_{h-a} \tag{5}$$

$$\boldsymbol{R}_{j-h} = \boldsymbol{R}_{j-sp} + \boldsymbol{R}_{sp-h} \tag{6}$$

$$R_{h-a} = T_h - \frac{T_a}{P_d}$$
(7)

where R_{j-h} is the thermal resistance between junction and the heat sink, R_{h-a} is the thermal resistance between heat sink and the ambient, R_{j-sp} is the thermal resistance between junction and the soldering point, R_{sp-h} is the thermal resistance between the soldering point and heat sink, T_h is the heat sink temperature. Multi-chip LED array die-spacing is important factor to determine the junction temperature [125]. Zhong, Zhang [126] proposed silicon-based heat sink packaging scheme in high power LED to reduce junction temperature.

Developments in LEDs are transforming the lighting industry by creating energy-efficient, durable and environmentally friendly designs of luminaries. However, addressing number of challenges are still required. Most important challenge is thermal management for significant heat created by LEDs. Three approaches of heat transfer are radiation, convection and conduction. However, conduction is the main approach of heat transfer in LEDs. The extensively used LEDs are limited to low power levels due to significant heat transfer through conduction. Contrary, high power LEDs have heat sink fixed in the base of LED case. When attached to specific types of printed circuit boards (PCBs) and other equipment for thermal management, the heat sink in LEDs provides the capacity of more watts in contrast to the widely used LEDs. Power consumption of 4 square inches of heat sink per watt is adopted as a practical and approximately used approach. This approach is effective when the LED is mounted on a PCB which is further mounted on aluminum or copper fixture behaving as a heat sink. Addition of heat sink can be a possibility, but problems like air flow and heat sink can occur if the heat sink is completely enclosed.

The recognized heat sinks are flat plates, die-cast finned heat sinks, and extruded finned heat sinks. However, for the selection of heat sinks surface area, aerodynamics, thermal transfer within the heat sink, flatness of the contact area and mounting methods shall be of great consideration [127]. Phosphor-based Light Emitting Diode (LED) is an increasingly attractive high-power high-brightness white light source. The LED chips in phosphor-based white LEDs, are layered with a phosphor-filled silicone. In the phosphor layer, blue light experiences Strokes shift, in which transformation from shorter wavelength (blue) to longer wavelength (yellow) takes place. The combination of blue and yellow light gives white light. Color rending index (CRI) and efficacy can be enhanced using

different colors of phosphors. In the phosphor layer, Stokes shift generates heat and absorbs light, resulting the increase in junction temperature chip. It also increases the temperature of phosphor itself if not dissipated efficiently [128,129]. The excessive temperature generated at the phosphor layer reduces the phosphor emission because of thermal quenching effect [130]. Moreover, damages to the critical components in the emitters and lamps, for example, lifetime reduction [131]. wavelength shift [132], silicone yellowing [133] and silicone carbonization [134] can be caused due to high temperature. Phosphors convert 8% of electrical power into heat Yan et al. [129]. In [135], it has proved that if the chip power efficiency is 30%, around 7% to 13% of electrical power provided to the LED chip is converted into heat by the phosphors. This is in consistent with the optical analysis performed by Yan et al. [129].

Reliability of high-power LEDs must consider while developing it. Stress temperature plays a major role in reliability assessment. Optimized stress temperature can assist to analyze the reliability of LED more effectively. It is found that flip and vertical chips enhance heat dissipation of LED more efficiently [136]. They also increase the lifetime of LED. This [137] investigate the thermal effect of phosphor based conformal and remote structures in LEDs. The junction and phosphor temperatures are measured using infrared (IR) thermometer. It is found that conformal structure LED has less phosphor temperature as compared to remote structure which show high heat conduction properties of conformal structure. In addition, lifetime of conformal structure is also better. Packaging of LED has vital contribution in thermal management of LED. Packaging consists of different levels, each of distinct materials. To obtain superior results in thermal management of packaging, each level of packaging along with its interface with other parts should be consider as high potential point. Nano materials and nano fabrication are commonly used to attain high efficiency of LED in smallest possible size [120]. Table III gives a review on different researches aimed to manage thermal issues of high-power LEDs.

| Refs. | Methodology | Findings | |
|--------------|--|---|--|
| Chakraborty | High power LEDs fabrication by mask design | Heat sinking of high-power LEDs is improved and | |
| et al. [138] | comprise of interdigitated multiplex arrays, also | provides better thermal management. | |
| | accompanied by extremely low series resistance. | | |
| Wang et al. | Pure aluminum heat pipe radiator with high | Heat pipe enhanced the thermal dissipation effect of LED. | |
| [139] | thermal conductivity is investigated for high | | |
| | power LEDs. | | |
| Peng et al. | Yellow YAG: Ce ³⁺ based phosphor-in-glass (PiG) | Improved thermal reliability | |
| [140] | packaging for high power white LED. | | |
| Yang et al. | Nano-twinned highly (111) textured copper film is | Enhancement in thermal management of high-power LED | |
| [141] | electrodeposited on back of the sapphire substrate. | chip. | |
| Yuan et al. | High power LED array packaging structure | Average die temperature reduces and heat dissipation | |
| [142] | integrated in microchannel cooler module. | ability of LED improved. | |

TABLE III: THERMAL MANAGEMENT TECHNIQUES FOR HIGH-POWER LED

| Ding et al. [143] | A 3058 lm high power single chip GaN-based white LED is fabricated and mounted on Cu heat dissipater. | Cu heat dissipater is significant in managing the heat produced by high power LED. | |
|------------------------|--|---|--|
| Tsai et al. [144] | High power white LED contain phosphor layer of Ce: YAG phosphor doped in glass. | Ce: YAG doped glass phosphor layer in LED exhibit high thermal stability than Ce: YAG doped silicon LED. | |
| Fan J et al. [145] | InGaN chip-based LED is attached to copper metal interconnect layer by silver adhesives | Higher thermal conductivity greater than 20 W/mK is achieved which is higher than 0.35 W/mK of conventional glass fiber epoxy material. | |
| Chen Z et al. [146] | Grephene oxid solution is coated on phosphor-in- glass (PiG) by oxidation reduction method in remote structured LED. | Thermal conductivity increased. Provide superior stability for high power LEDs. | |

VIII. VISIBLE LIGHT EMISSION THROUGH NANOMATERIALS

In recent years, two-dimensional nanomaterials like graphene and molybdenum disulfide (MoS₂) are investigated for emission of visible light. These analyses include formation of vertical van der Waals heterostructures by stacking distinct two-dimensional semiconductors on top of each other, functioning as LEDs [147]. Nanomaterials emit thermal light at high electron temperature which is achieved by reducing heat dissipation in vertical direction through substrate. Graphene is favorable for optoelectronics applications such as non-linear photonic devices. Graphene on silicon dioxide (SiO₂) substrates are demonstrated useful as emitters in mid-infrared ranges but bright and broadband light emission in visible range is difficult. This is because the recombination of electron and hole is graphene is inefficient. Superior thermal stability, low heat capacity and high strength of graphene make him suitable for thermal light emission. In past, degradation of dielectric at high temperature limit graphene to infrared range. However, in presence of rapid cooling, temperature of 2800 K is achieved for thermal light emission in visible range [148]. The interaction of emitted thermal light and reflected light from substrate surface give rise to interference effect which can be utilized to change the wavelength of emitted light [149]. Graphene is robust to support high temperatures and high current densities along with rapid decrease in thermal conductivity. These properties make graphene suitable to work as nanoscale light emitter. Withers F. et al. [147] demonstrate graphene based efficient LED. For this purpose, they carefully stack graphene as a transparent conductive layer, hexagonal boron nitride and semiconducting monolayers.

Transition metal dichalcogenides (TMD) semiconductors having high excitonic photoluminescence and electroluminescence are under research to explore their light emission capabilities. One of its compound molybdenum disulfide (MoS_2) reveal some special properties such as low thermal conductivity which is useful for emission of visible light. Vertical heat dissipation of this material can be highly reduced by suspending the material in vacuum. Dobusch L et al. [150] showed that monolayer MoS_2 can achieve high temperature (around 1600 K) in low oxygen partial pressure in vacuum chamber to emit visible light as a result of joule heating.

IX. DISCUSSION AND CONCLUSION

White-LEDs are fourth generation source of solid-state lighting. Due to growing demand of white-LEDs in market, manufacturers are in search of superior methods to develop them. RGB and phosphor are two main coating approaches to develop white-LEDs. The RGB based white LEDs are advantageous due to flexibility of having multicolor. However, this is not adopted by manufacturers because it requires complex electrical design for output color management. The second method of phosphor coating is more mature and comparatively cheap technology. Its production is easy and good CRI (70-90+) can be achieved. Among phosphor coating configurations, the remote configuration is superior as it resolves the problem of backscattering light by employing SPE method. However, in conformal configuration, luminous efficiency is less because 50-60% of light is backscattered from phosphor layer even if it is uniformly deposited to obtain high ACU. The dispersed configuration has least luminous efficiency because the non-uniform phosphor distribution results in poor ACU. The life of high-power LEDs is greatly affected by high temperature. Hence, they are coupled with heat sinks which greatly reduces its temperature. Table IV presents the pros, cons along with scope of different white-LED technologies. As LEDs are built with precisely deposited layers of semiconductor materials, tiny changes in thickness of phosphor layer will lead to color changes in LEDs. Furthermore, they are more efficient than incandescent bulbs as they convert half of their electricity to light. Many challenges are still needed to be addressing to attain desirable rate and efficiency. For example, the process of down-conversion offers room for improvement. Additionally, areas such as current efficiency droop, color shift, system reliability, light distribution, dimming, thermal management and power supply performance needs consideration. Improvement in luminaire cost, performance and design flexibility can be done by minimizing high temperature efficiency losses and driving situations for current operation.

TABLE IV Assessment of Different White-LED Technologies

| White-LED | Advantages | Disadvantages | Scope |
|----------------|-------------------------------------|---|-------------------------------------|
| RGB based | Flexible to achieve desire shade of | Unstable white light after a time frame | Limited due to its high cost. |
| | white color. | and have low CRI. | |
| Phosphor based | Stable white light, high luminous | Lack red emission for very high CRI | Wide availability due to simple and |
| | flux and relatively good CRI. | and have narrow color gamut. | inexpensive manufacturing. |
| Three band | Stable, high CRI (>90) and wide | Phosphor starts to saturate at higher | High demand where high CRI is |
| | color gamut. | current, causes gradual decrease in | required such as museum gallery |
| | | CRI. | and in hospitals. |
| High power | High luminance. | Overheating due to high driven | Leading lighting technology by |
| | | current | 2025. |

X. References

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